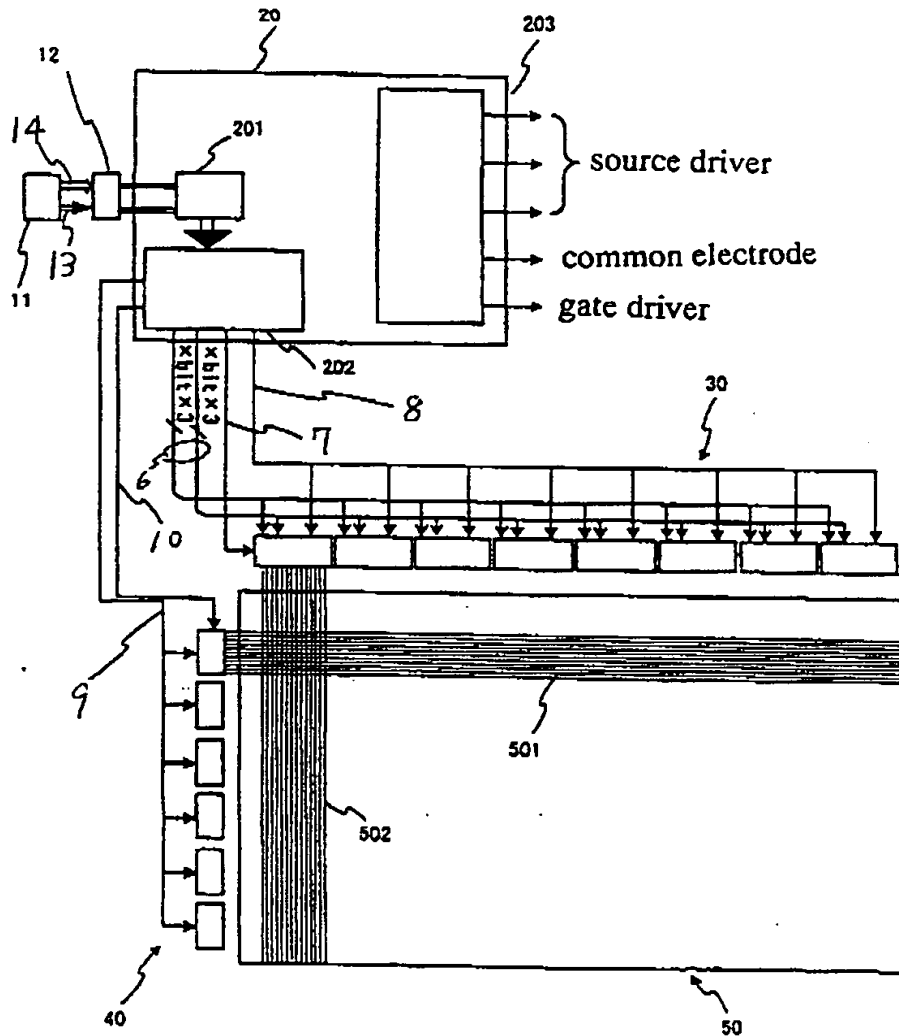
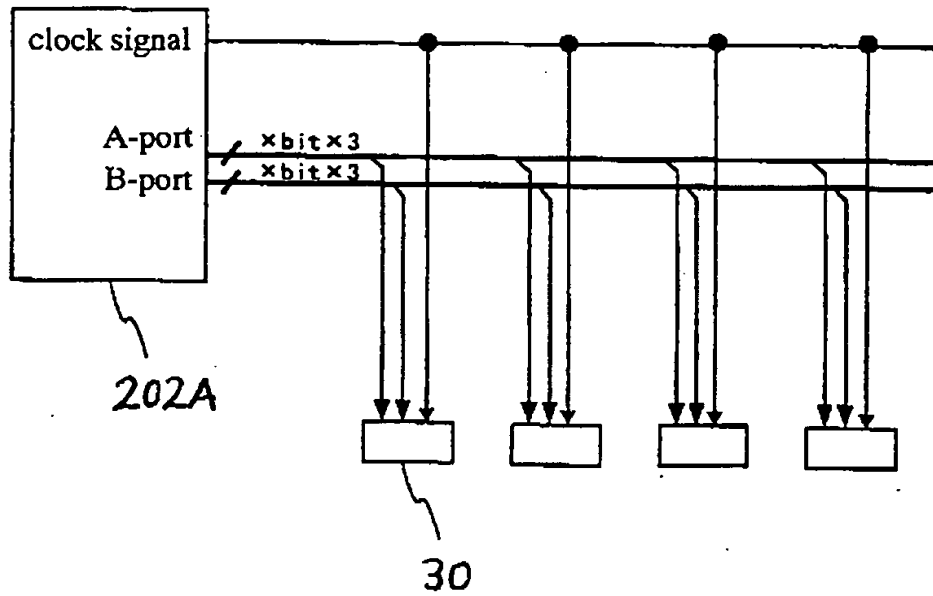


FIG. 1 prior art



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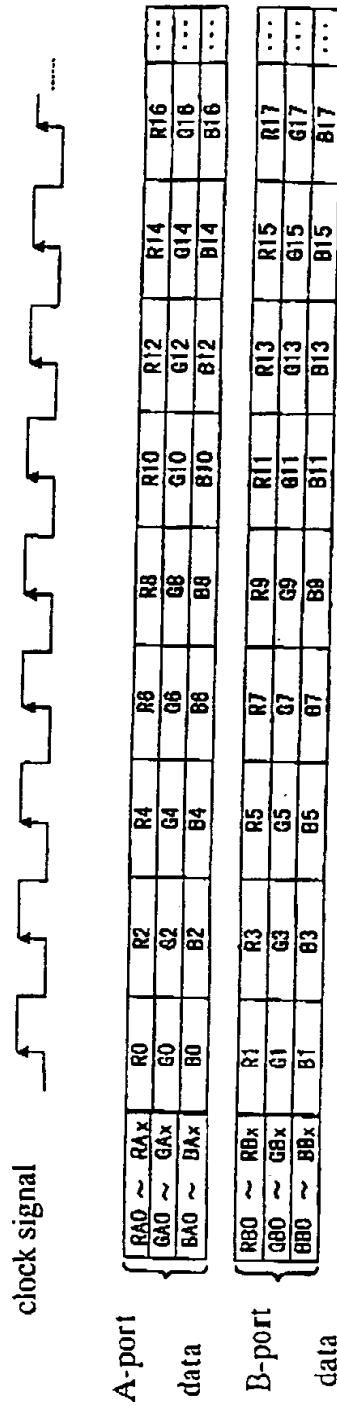
FIG. 2 prior art



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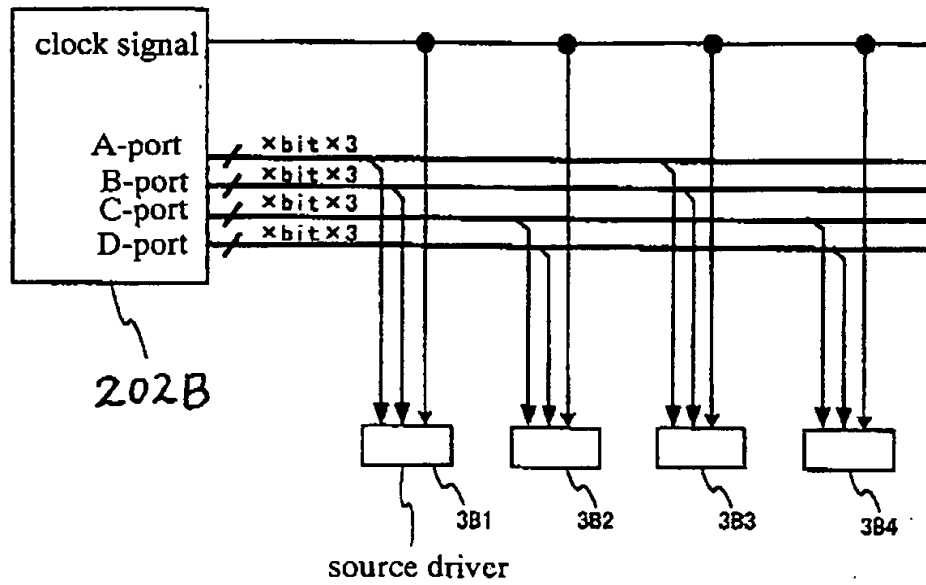
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FIG. 3 prior art



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FIG. 4 prior art



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FIG. 5 prior art

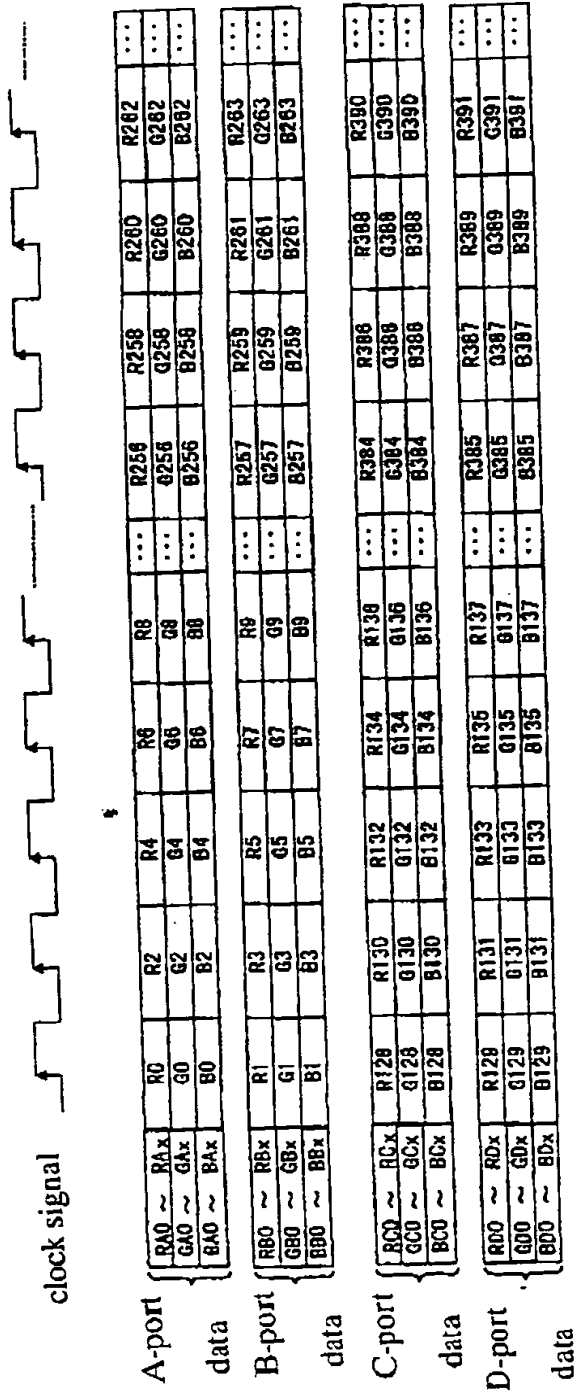
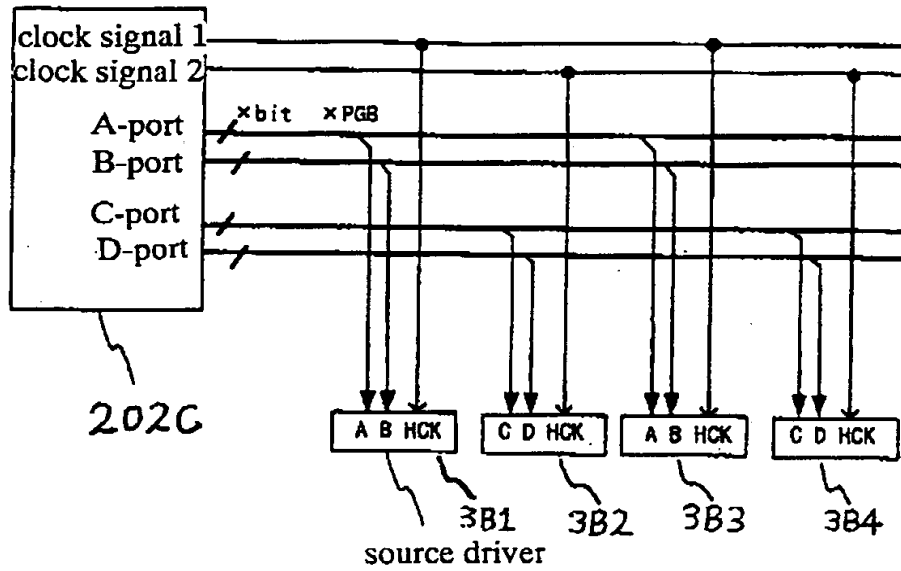


FIG. 6 prior art



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FIG. 7 prior art

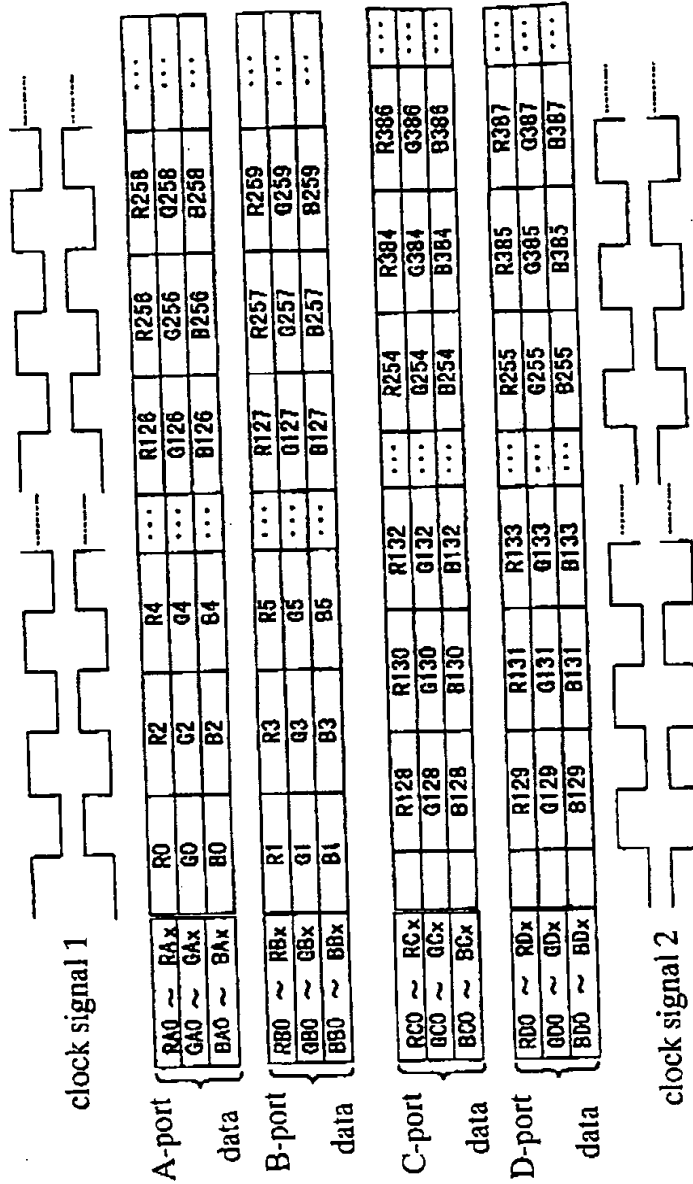
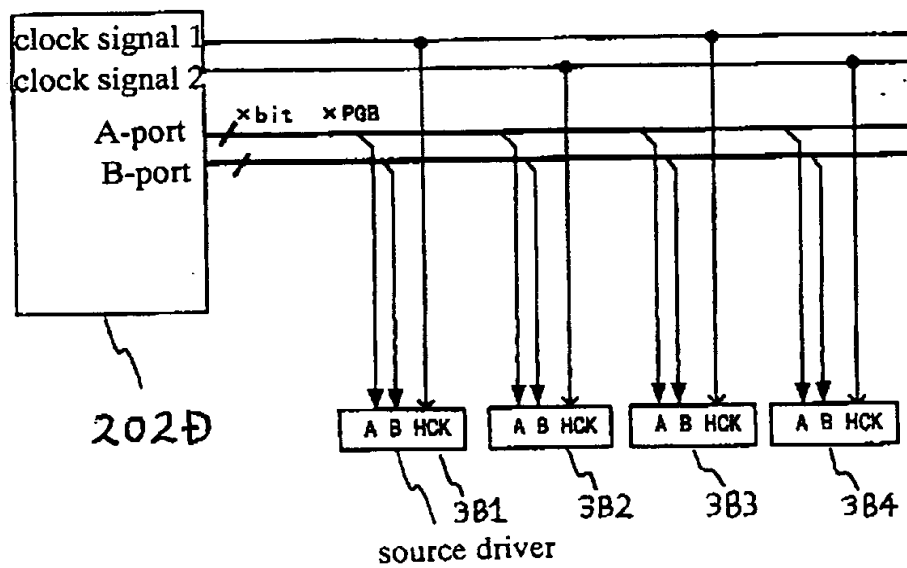


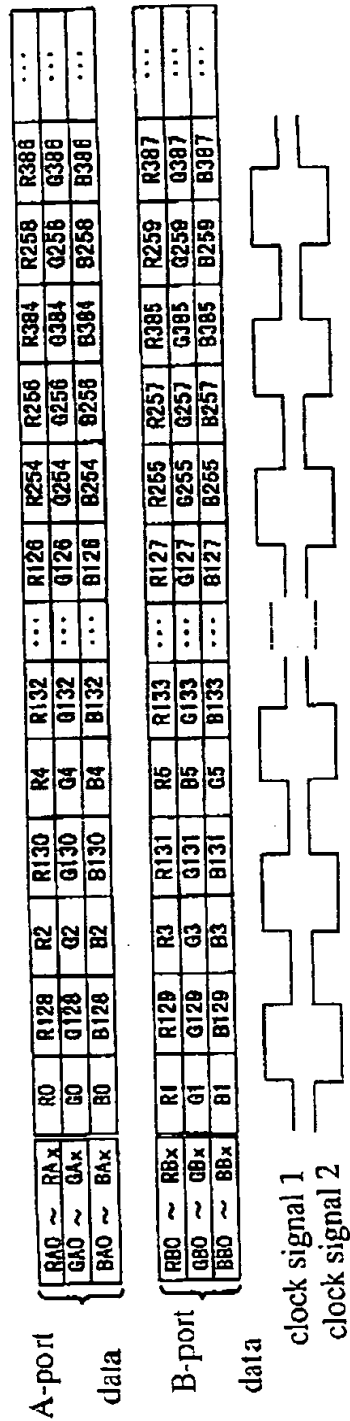
FIG. 8 prior art



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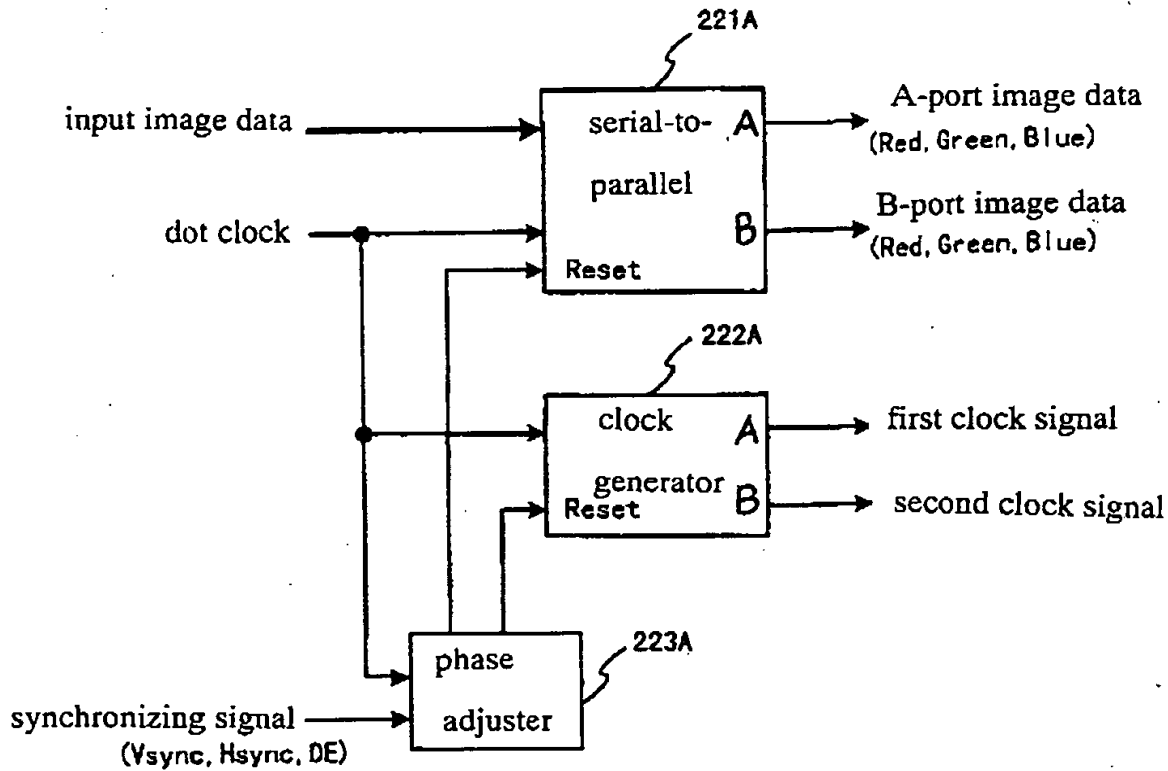
FIG. 9 prior art



[illegible]

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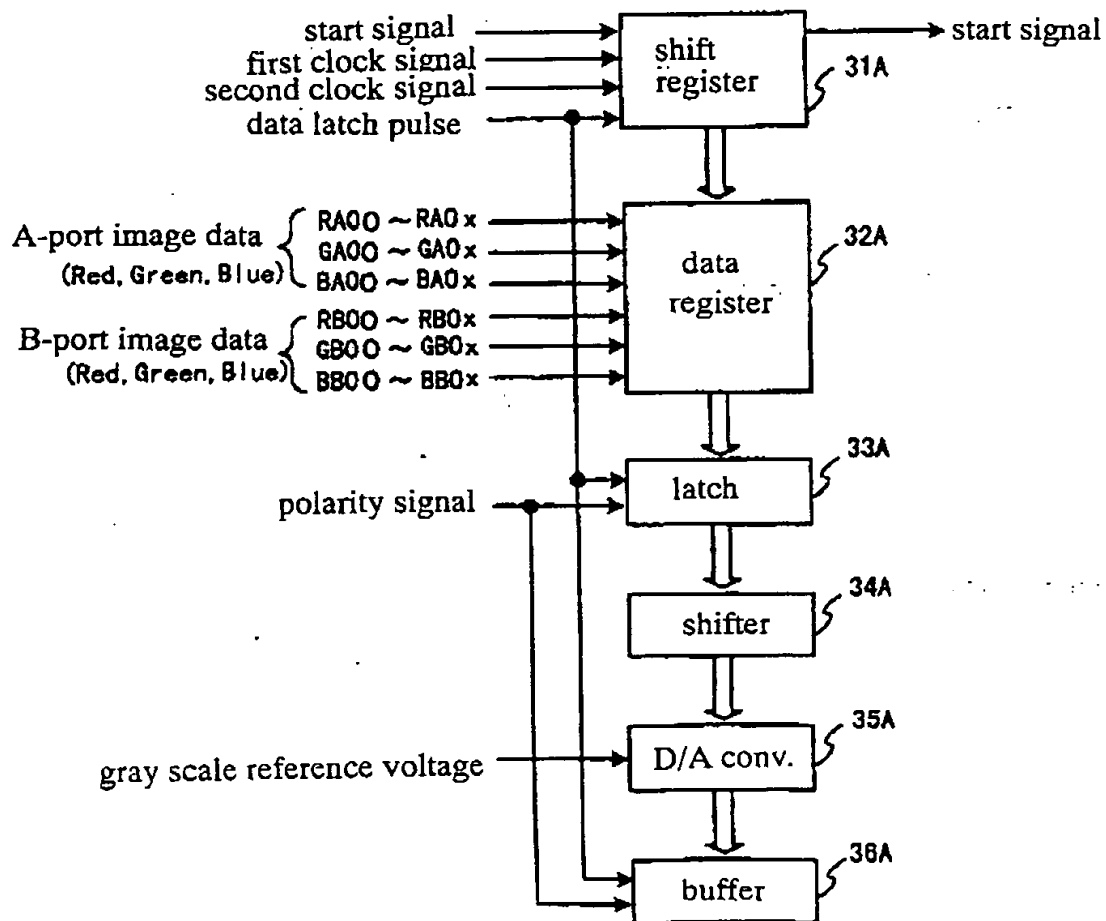
FIG. 11



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FIG. 12



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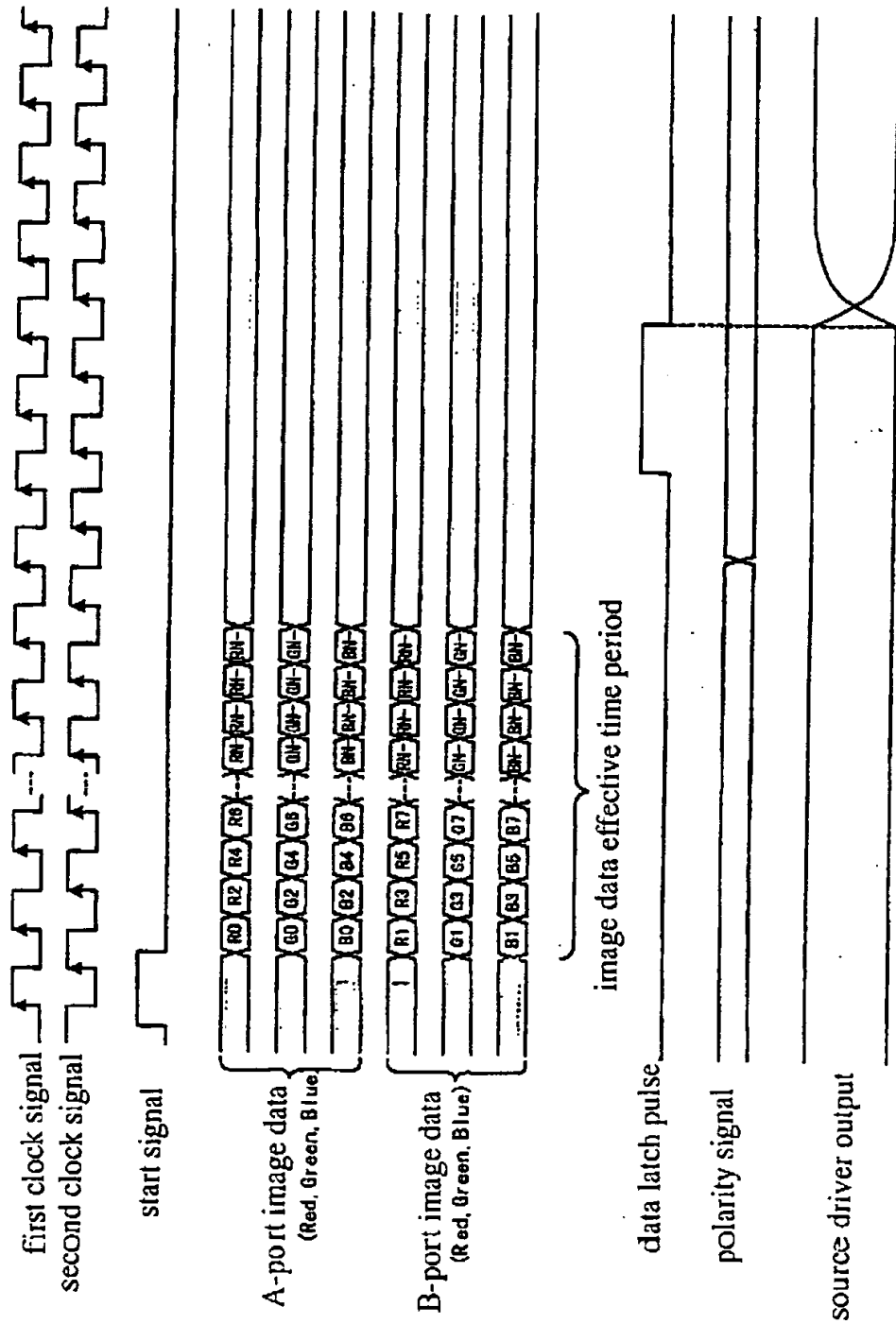


FIG. 14A

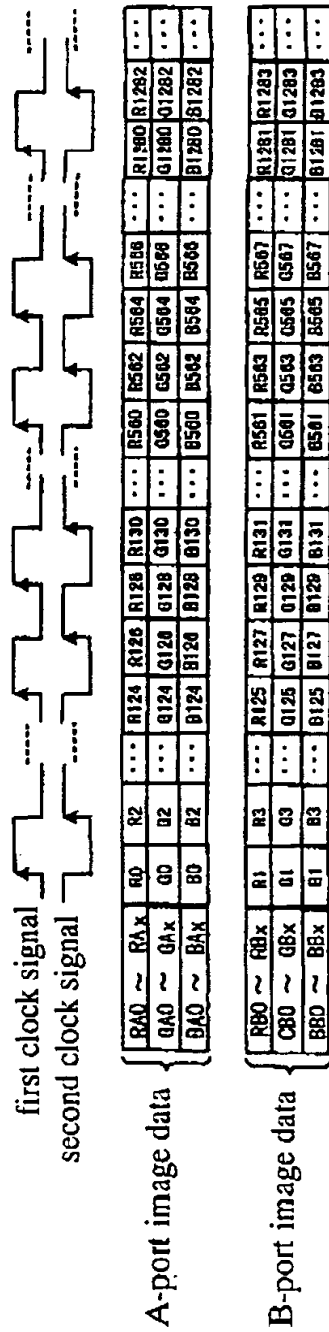
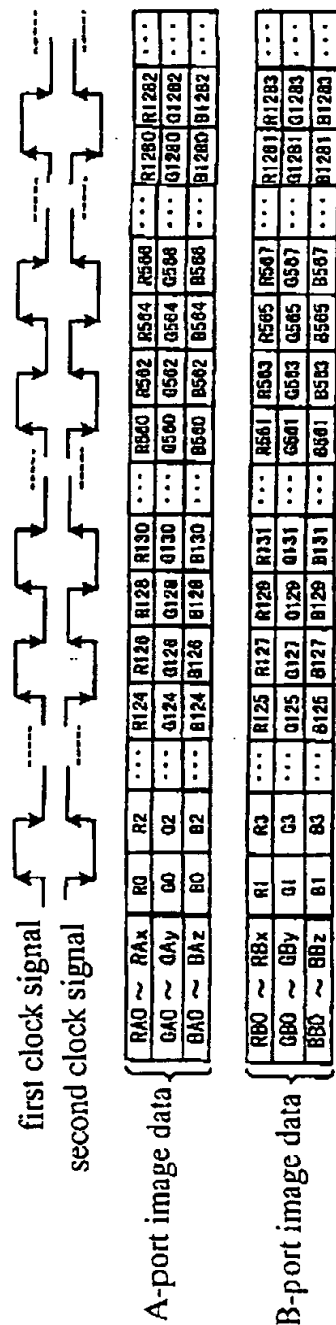
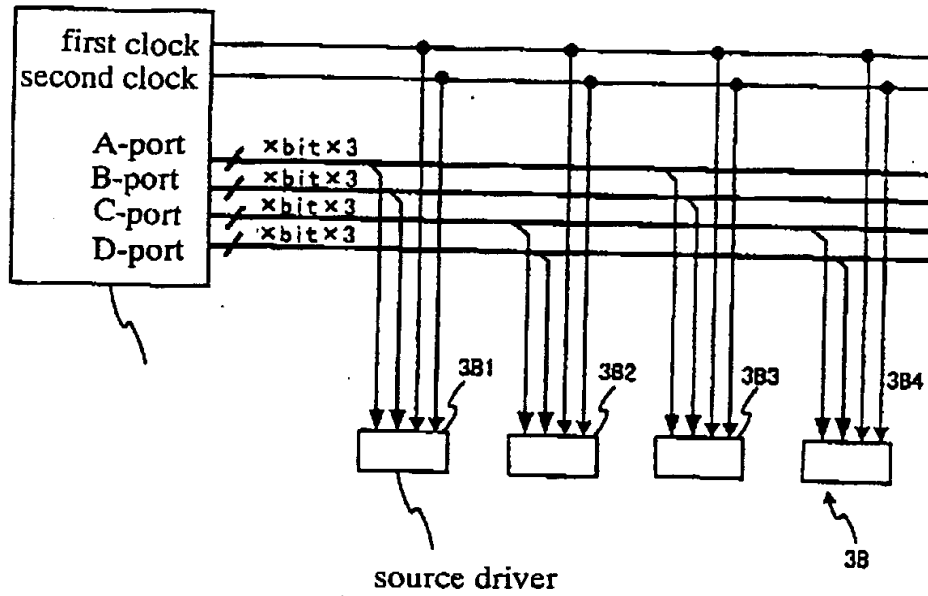


FIG. 14B



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FIG. 15

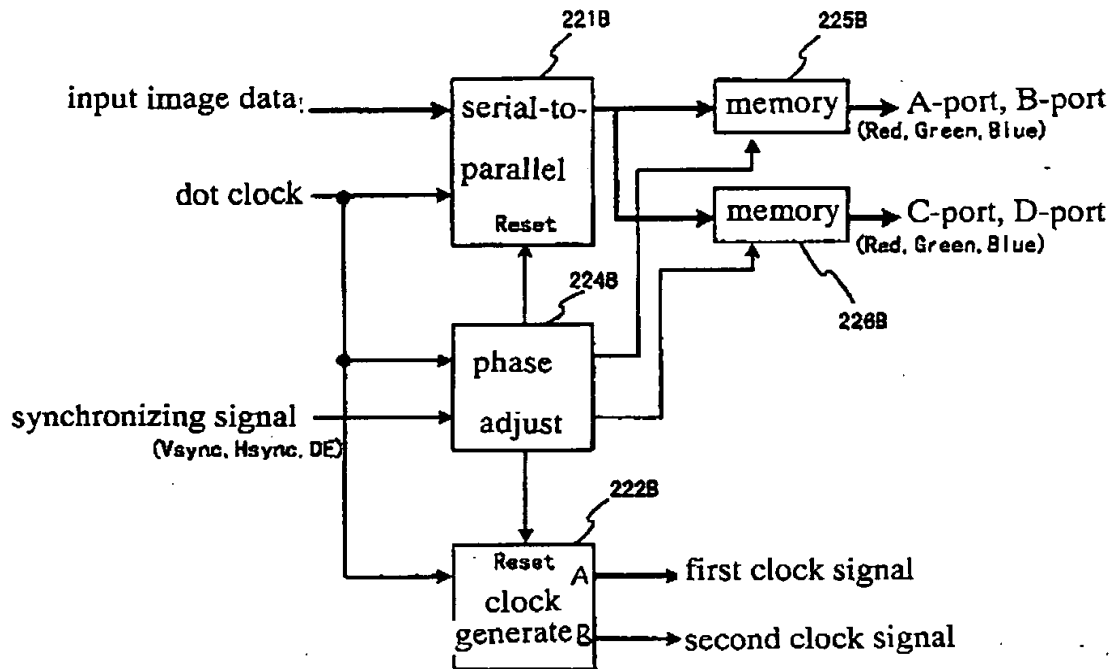


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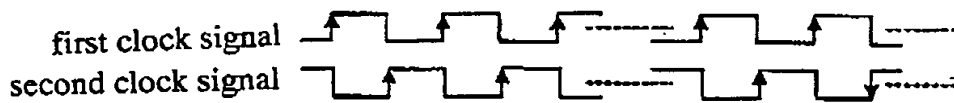
FIG. 16



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FIG. 17

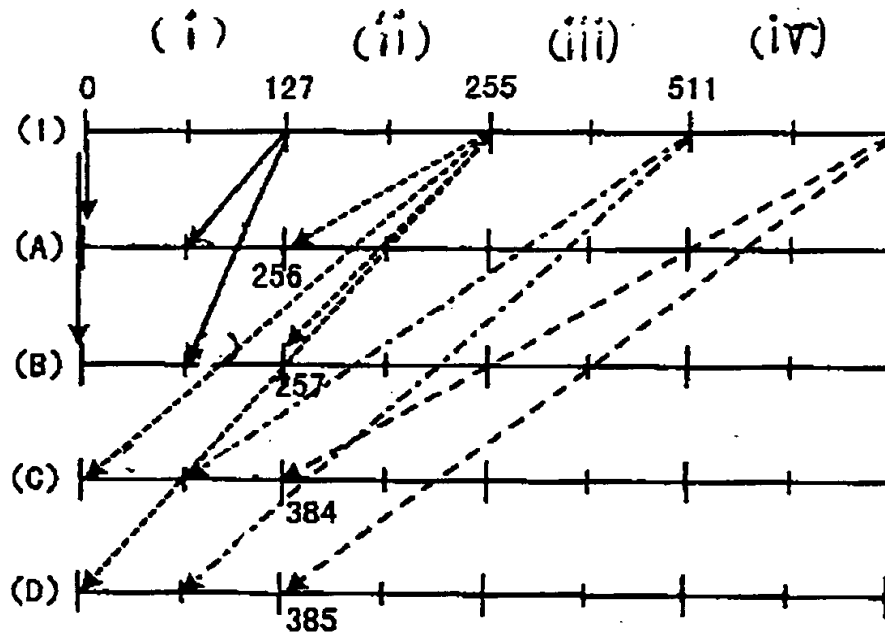


A-port	RA0 ~ RAx	R0	R2	R4	R6	R8	---	R256	R258	R260	R262	---
	GA0 ~ GAx	G0	G2	G4	G6	G8	---	G256	G258	G260	G262	---
	BA0 ~ BAx	B0	B2	B4	B6	B8	---	B256	B258	B260	B262	---
B-port	RB0 ~ RBx	R1	R3	R5	R7	R9	---	R257	R259	R261	R263	---
	GB0 ~ GBx	G1	G3	G5	G7	G9	---	G257	G259	G261	G263	---
	BB0 ~ BBx	B1	B3	B5	B7	B9	---	B257	B259	B261	B263	---
C-port	RC0 ~ RCx	R128	R130	R132	R134	R136	---	R384	R386	R388	R390	---
	GC0 ~ GCx	G128	G130	G132	G134	G136	---	G384	G386	G388	G390	---
	BC0 ~ BCx	B128	B130	B132	B134	B136	---	B384	B386	B388	B390	---
D-port	RD0 ~ RDx	R129	R131	R133	R135	R137	---	R385	R387	R389	R391	---
	GD0 ~ GDx	G129	G131	G133	G135	G137	---	G385	G387	G389	G391	---
	BD0 ~ BDx	B129	B131	B133	B135	B137	---	B385	B387	B389	B391	---

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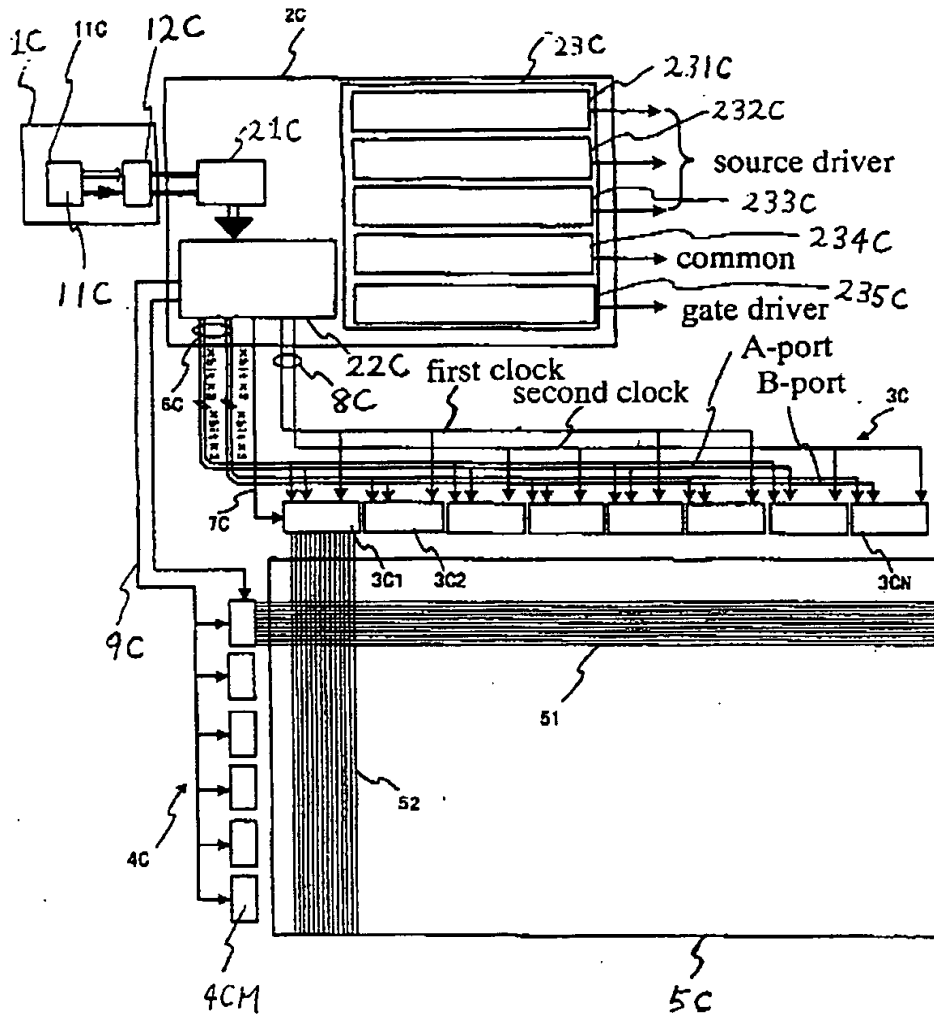
FIG. 18



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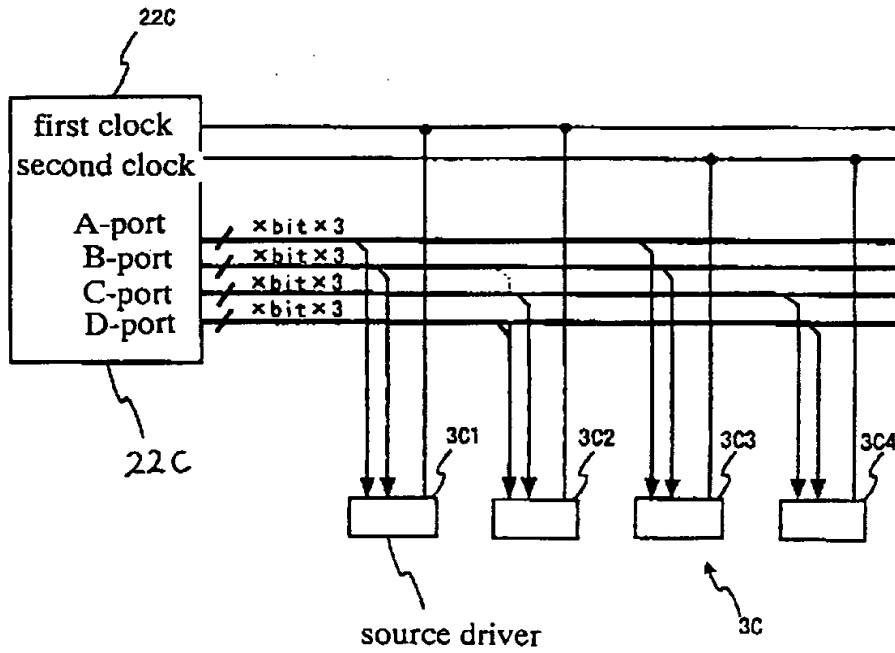
FIG. 19



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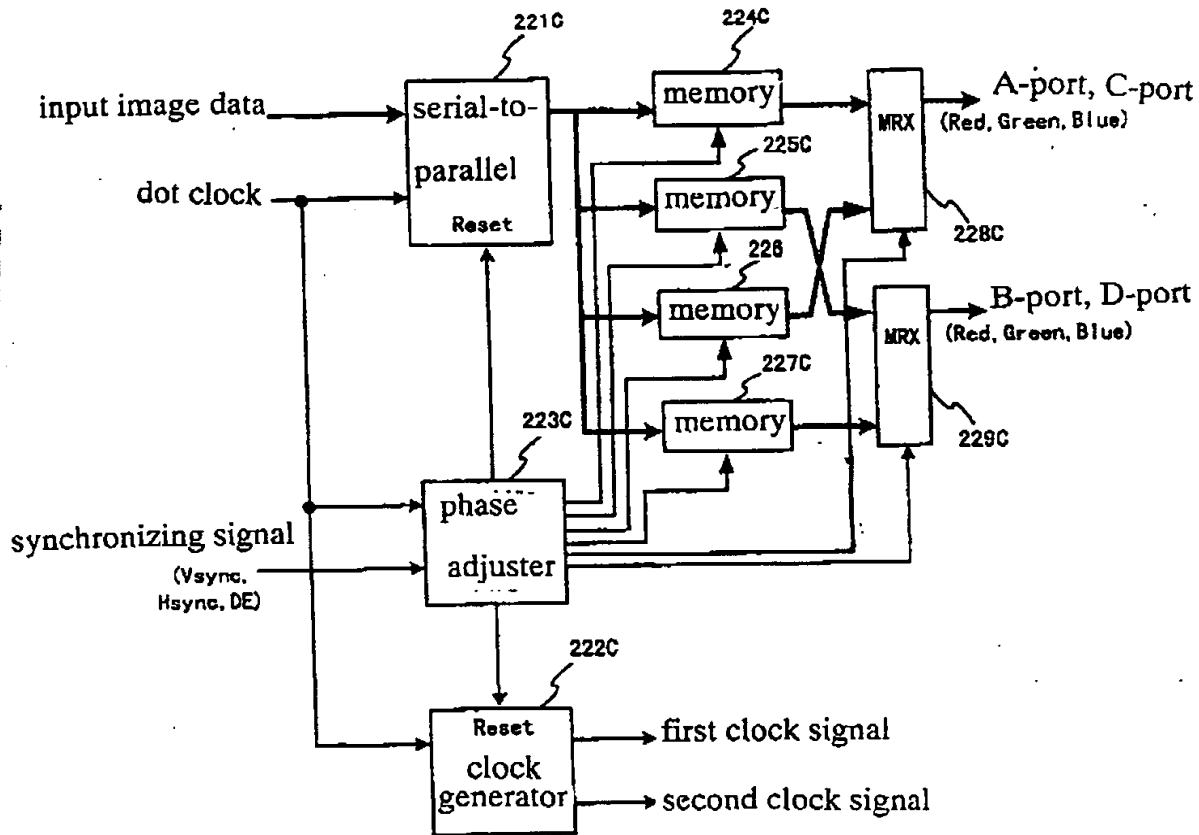
FIG. 20



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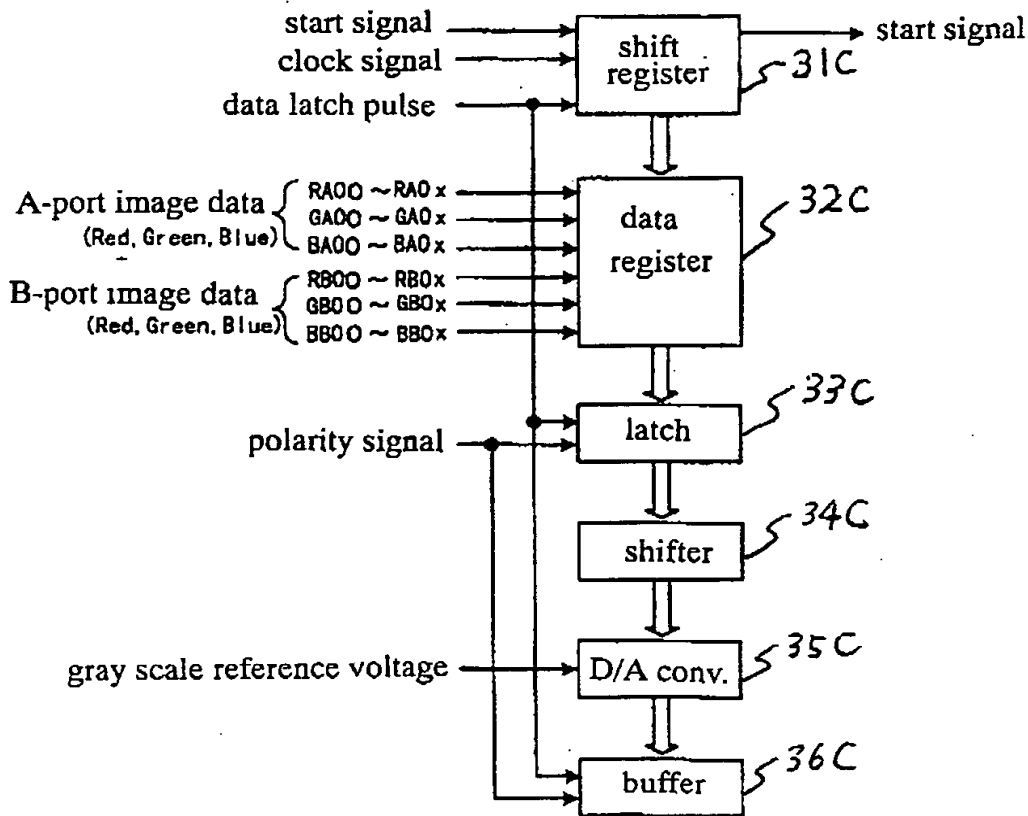
FIG. 21



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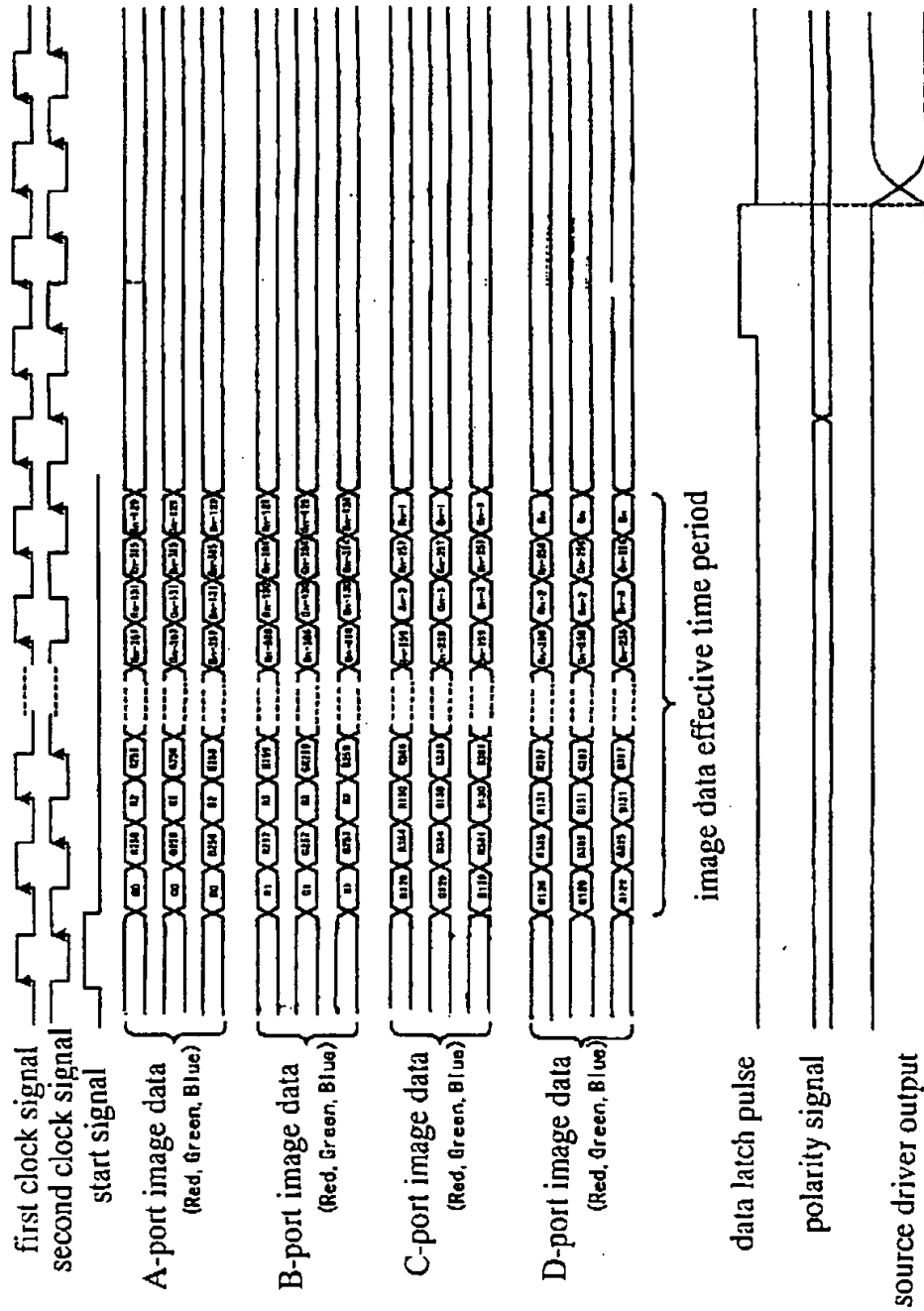
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FIG. 22



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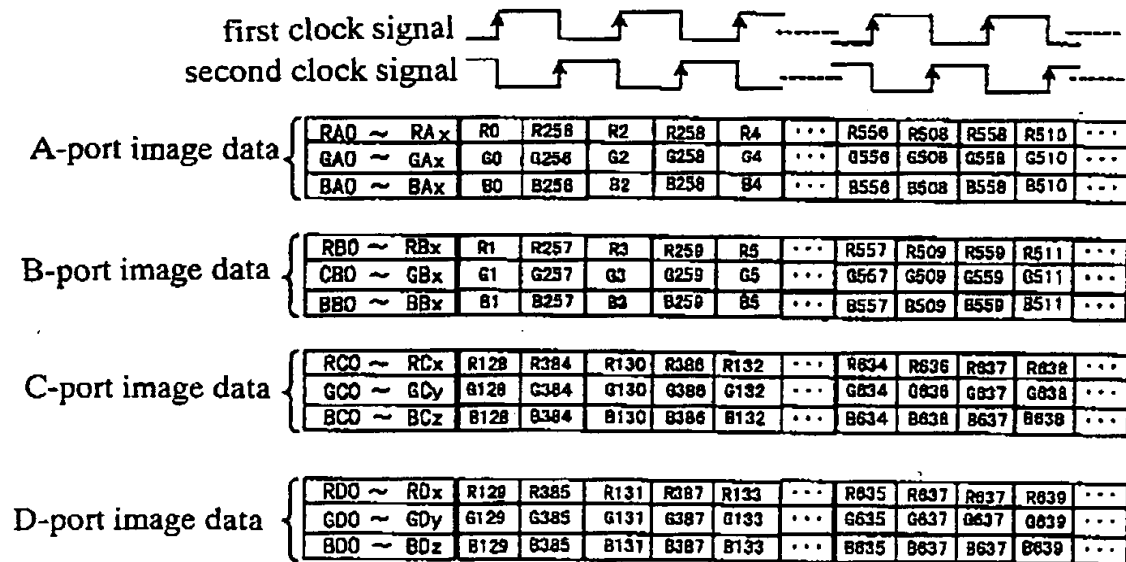
FIG. 23





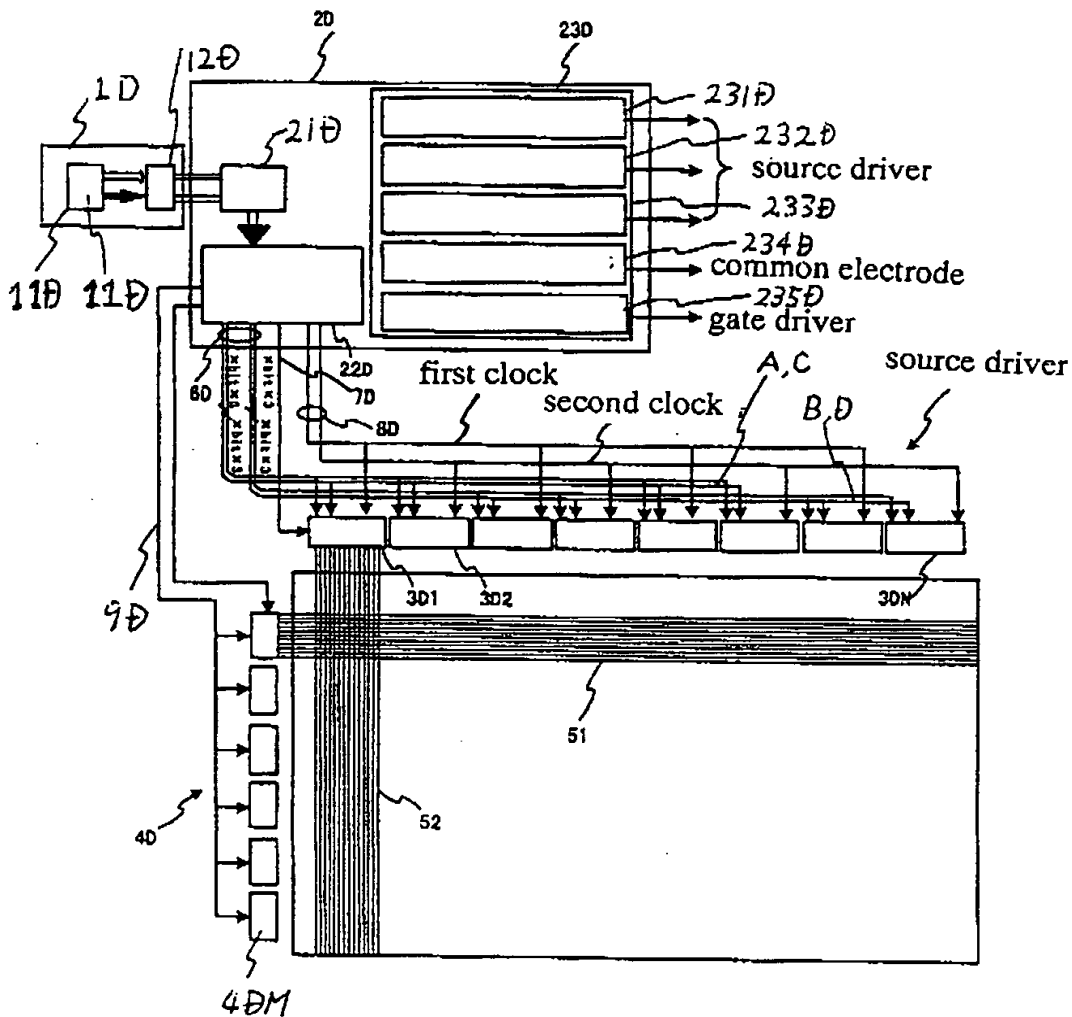
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FIG. 24



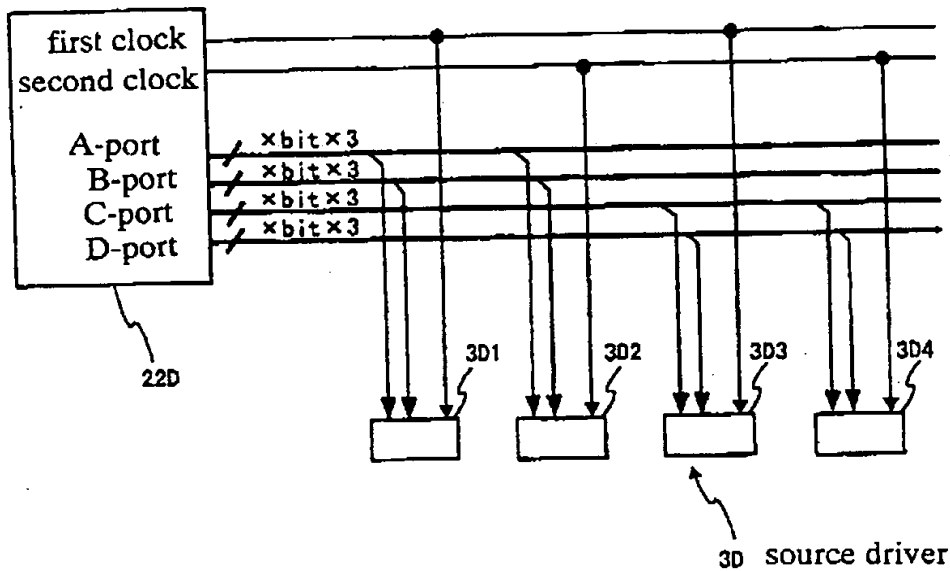
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FIG. 25



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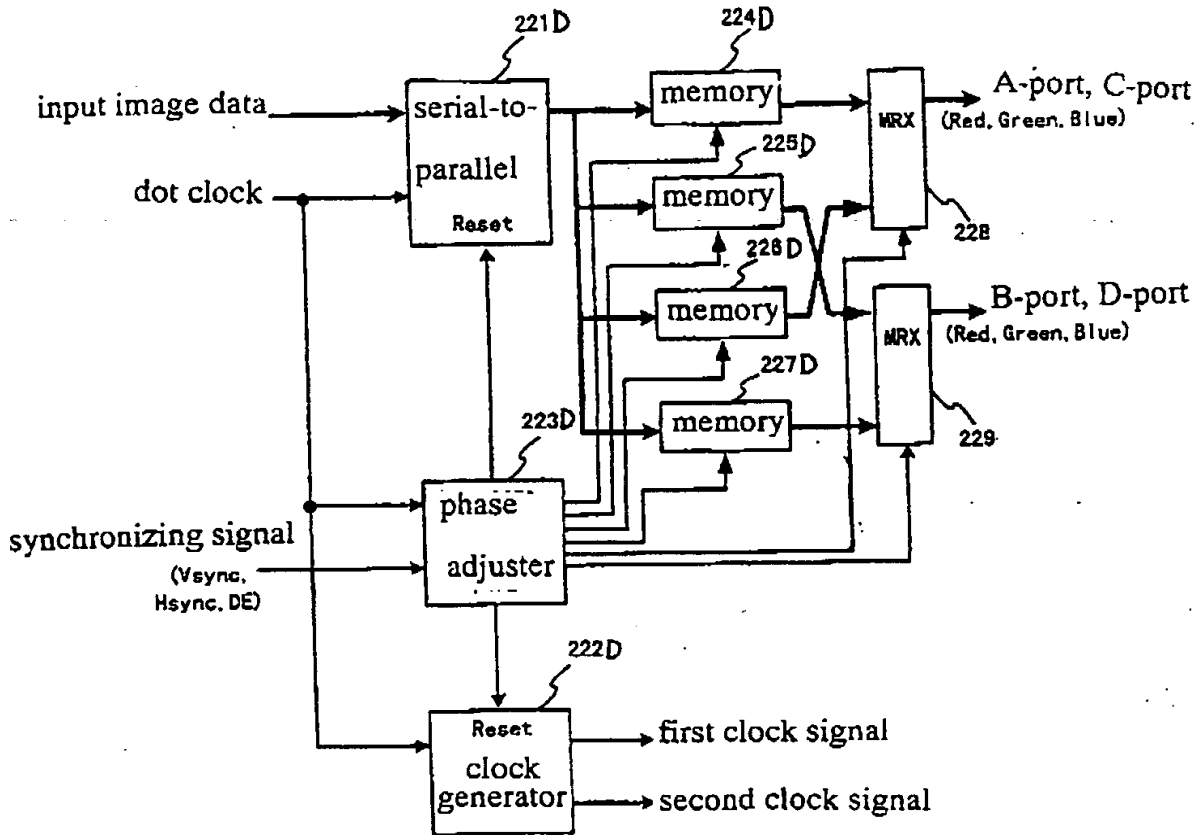
FIG. 26



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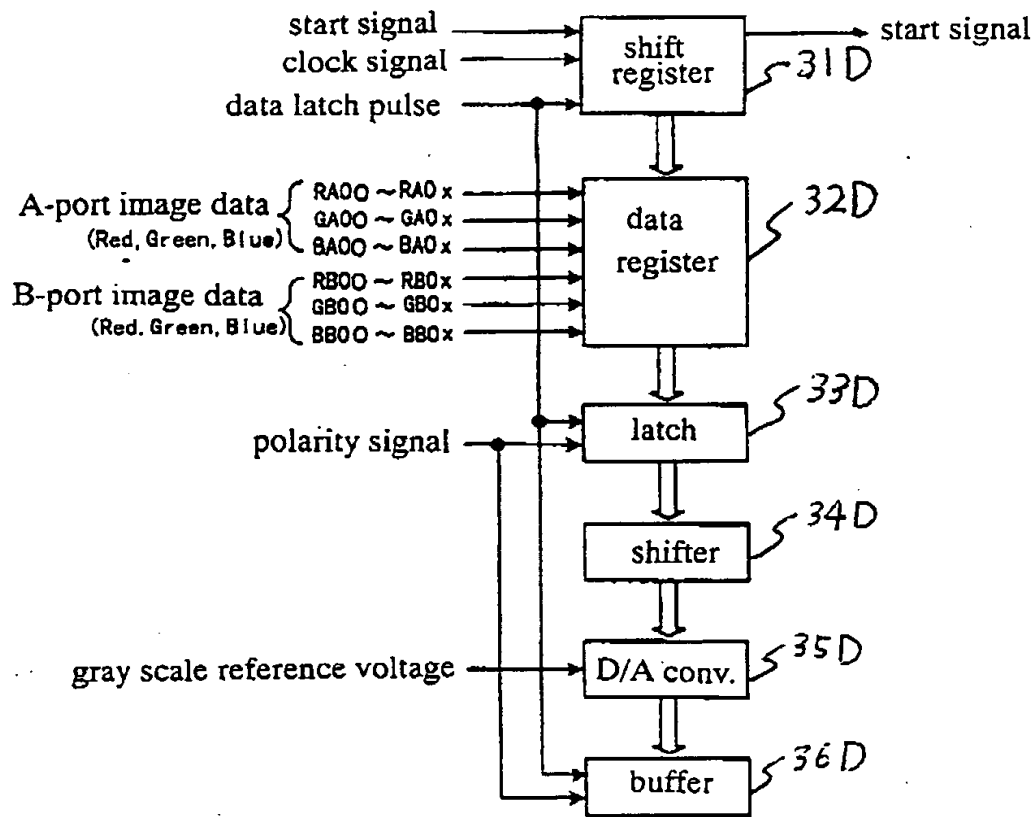
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FIG. 27



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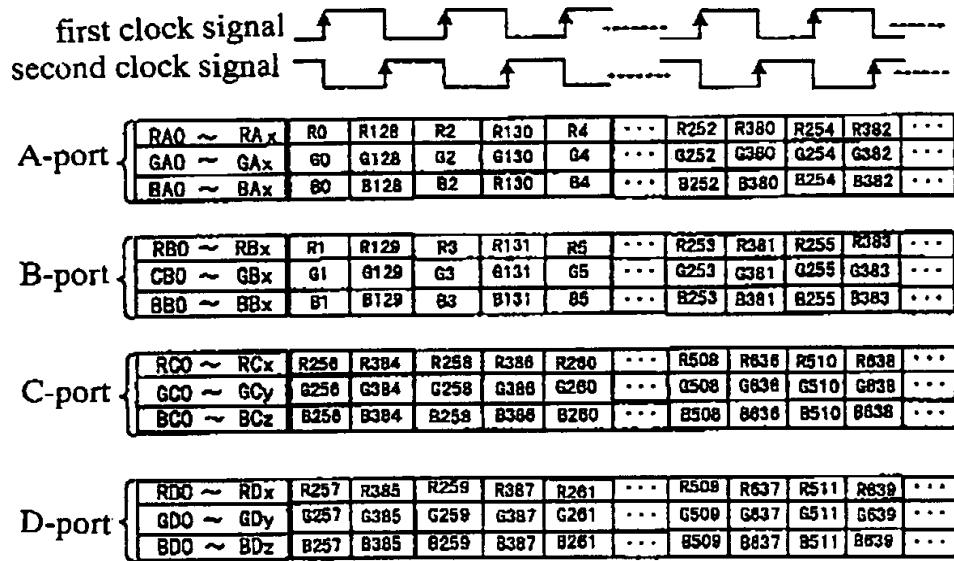
FIG. 28



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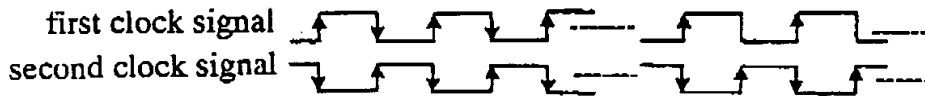
FIG. 29



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FIG. 30



A-port {

RA0 ~ RA <sub>x</sub>	RD	R128	R2	R130	R4	...	R512	R640	R514	R642	...
GA0 ~ GA <sub>x</sub>	GD	G128	G2	G130	G4	...	G512	G640	G514	G642	...
BA0 ~ BA <sub>x</sub>	BD	B128	B2	B130	B4	...	B512	B640	B514	B642	...

B-port {

RBD ~ RB <sub>x</sub>	R1	R129	R3	R131	R5	...	R513	R641	R515	R643	...
GBD ~ GB <sub>x</sub>	G1	G129	G3	G131	G5	...	G513	G641	G515	G643	...
BBG ~ BB <sub>x</sub>	B1	B129	B3	B131	B5	...	B513	B641	B515	B643	...

C-port {

RCD ~ RC <sub>x</sub>	R256	R384	R258	R386	R260	...	R768	R896	R770	R898	...
GCD ~ GC <sub>x</sub>	G256	G384	G258	G386	G260	...	G768	G896	G770	G898	...
BCD ~ BC <sub>x</sub>	B256	B384	B258	B386	B260	...	B768	B896	B770	B898	...

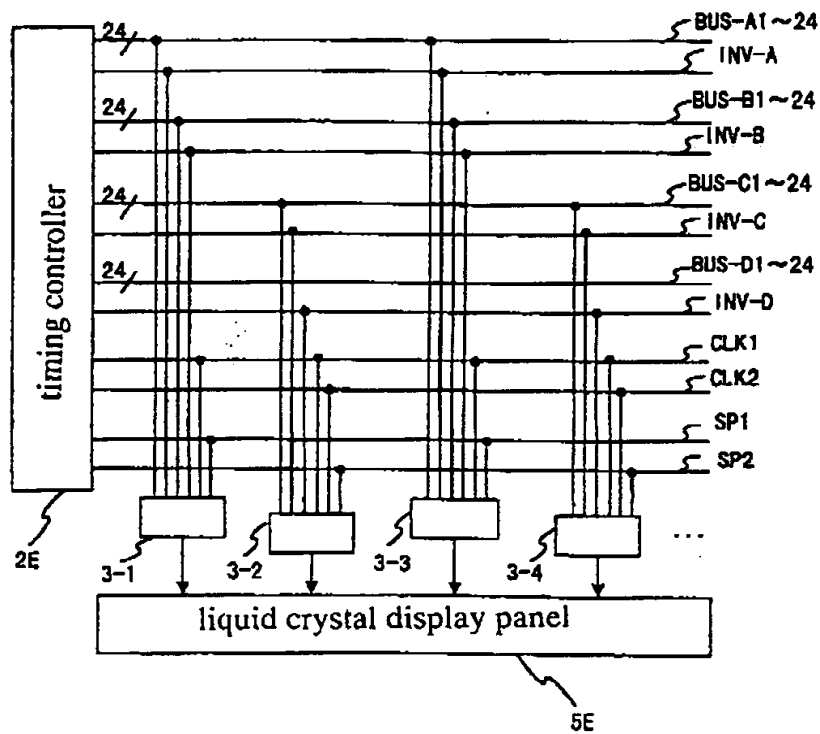
D-port {

RDD ~ RD <sub>x</sub>	R257	R385	R259	R387	R261	...	R769	R897	R771	R899	...
GDD ~ GD <sub>x</sub>	G257	G385	G259	G387	G261	...	G769	G897	G771	G899	...
BDD ~ BD <sub>x</sub>	B257	B385	B259	B387	B261	...	B769	B897	B771	B899	...

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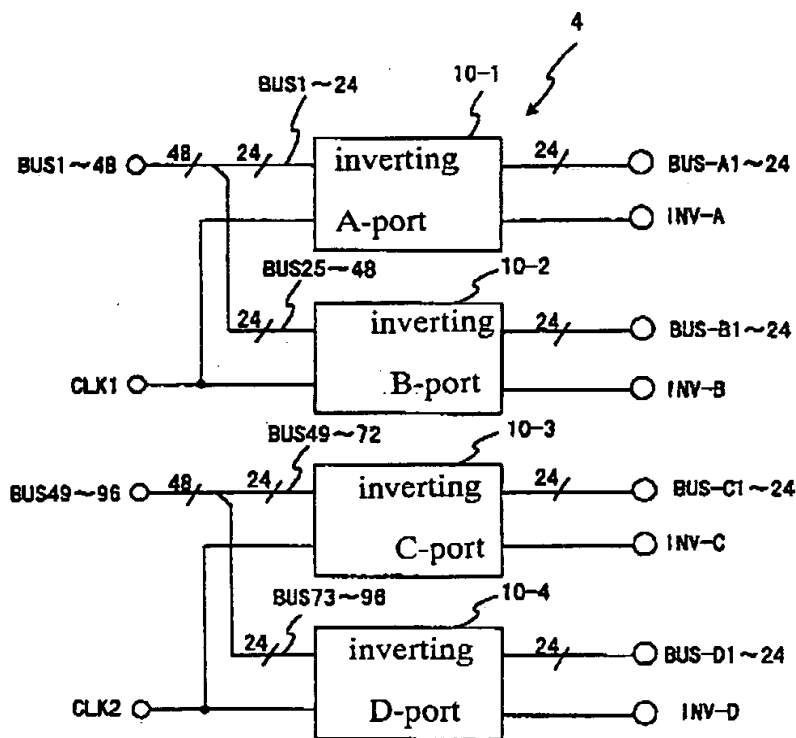
FIG. 31



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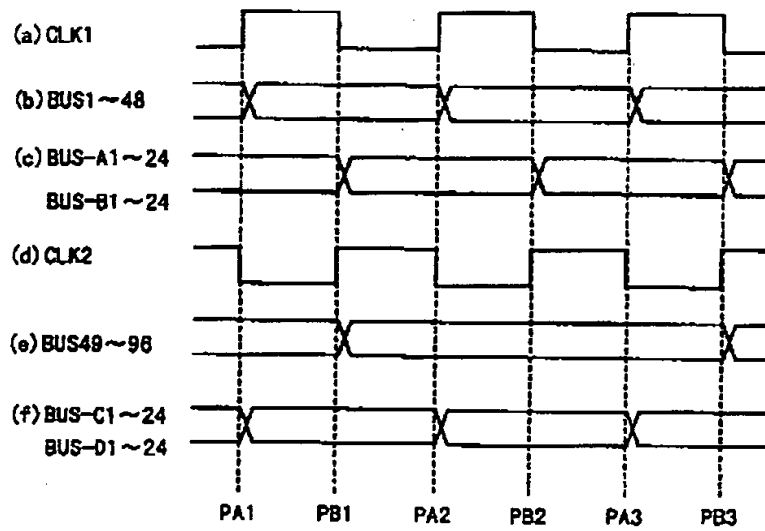
FIG. 32



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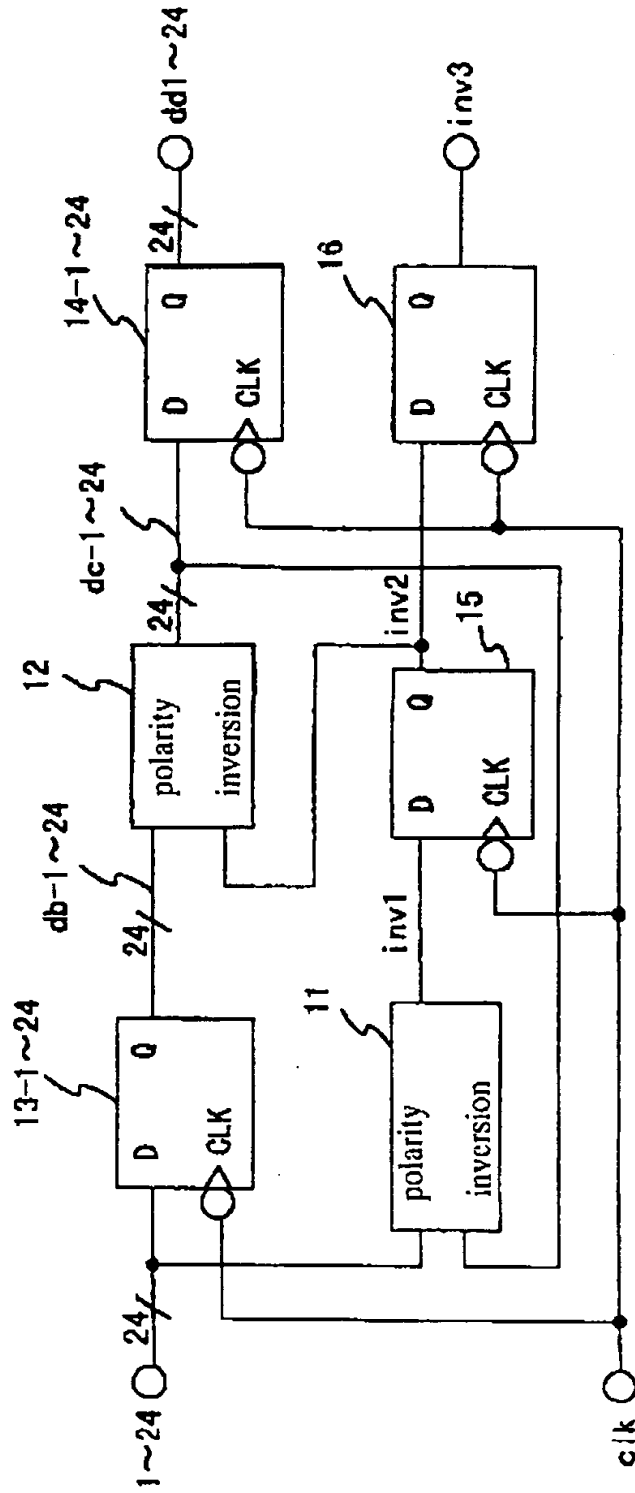
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FIG. 33



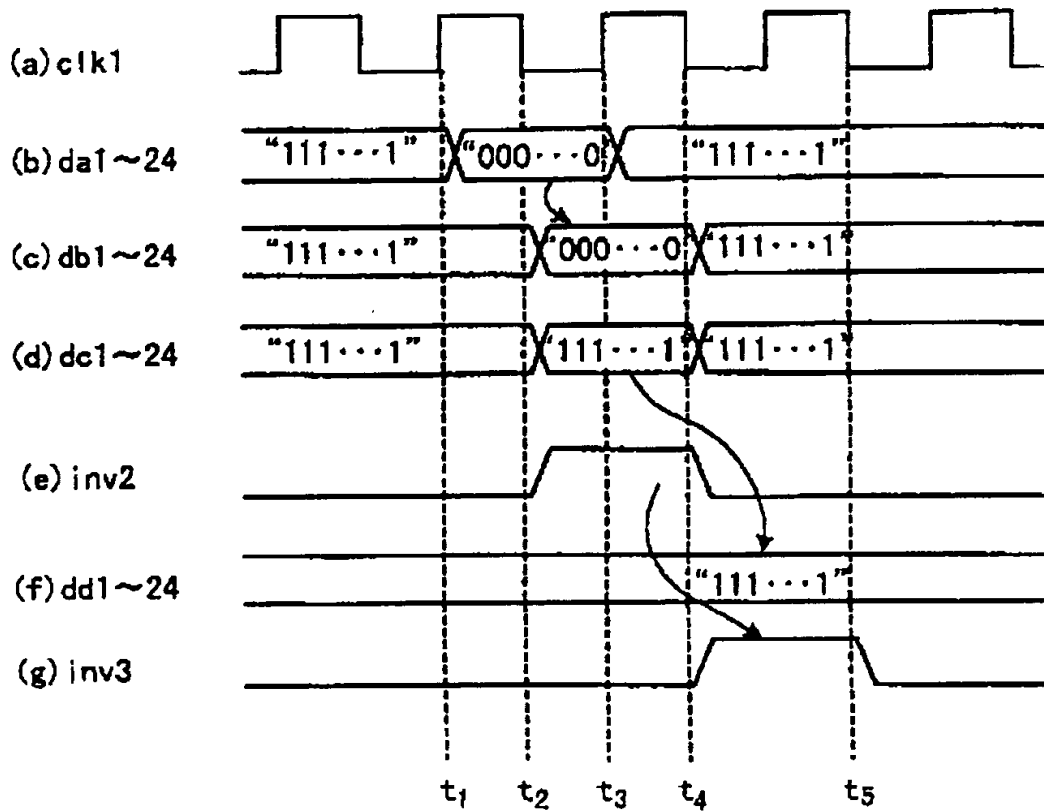
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FIG. 34



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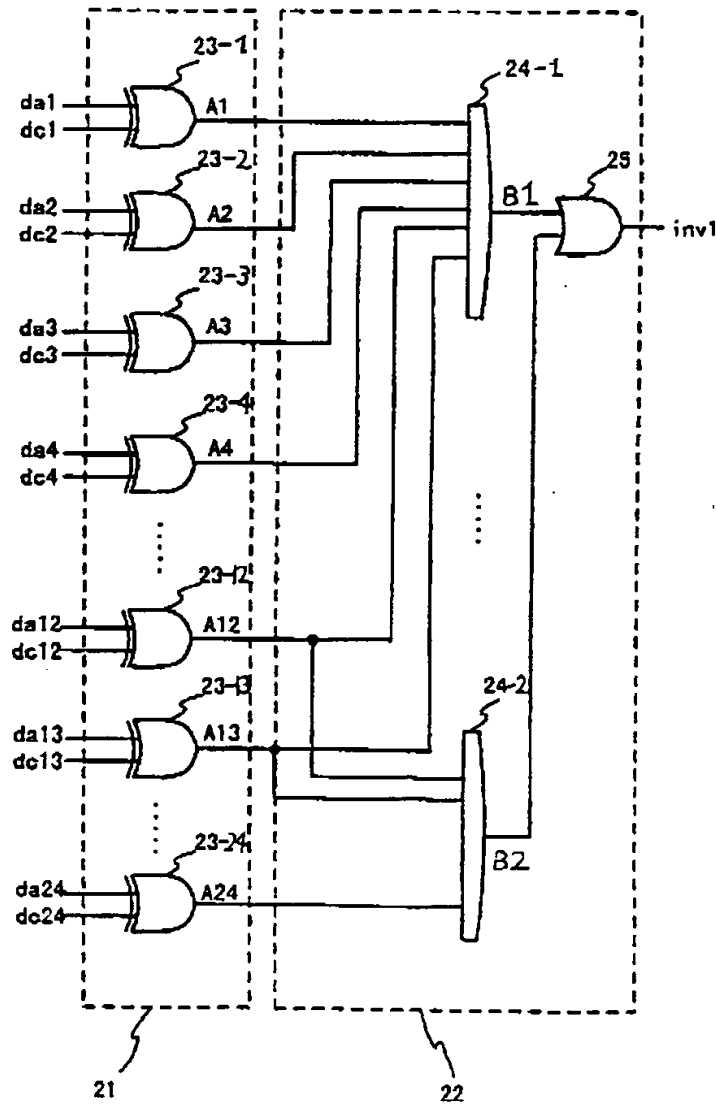
FIG. 35



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FIG. 36



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FIG. 37

n	1	2	3	4	5	.....	22	23	24
dan	H	H	L	H	H	.....	H	H	H
dcn	H	L	H	L	L	.....	H	L	H
An	L	H	H	H	H	.....	L	H	L

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FIG. 38A

n	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Xn	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L
Yn	H	H	H	H	H	H	H	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L
Zn	H	H	H	H	H	H	H	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L

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FIG. 38B

n	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Xn	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L
Yn	H	H	H	H	H	H	H	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L
Zn	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L

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FIG. 38C

n	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Xn	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L
Yn	H	H	H	H	H	H	H	H	L	L	L	L	H	H	H	H	L	L	L	L	L	L	L	L
Zn	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L

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FIG. 38D

n	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
Xn	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L
Yn	H	H	H	H	H	H	H	H	H	L	L	L	H	H	H	L	L	L	L	L	L	L	L	L
Zn	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L

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FIG. 39

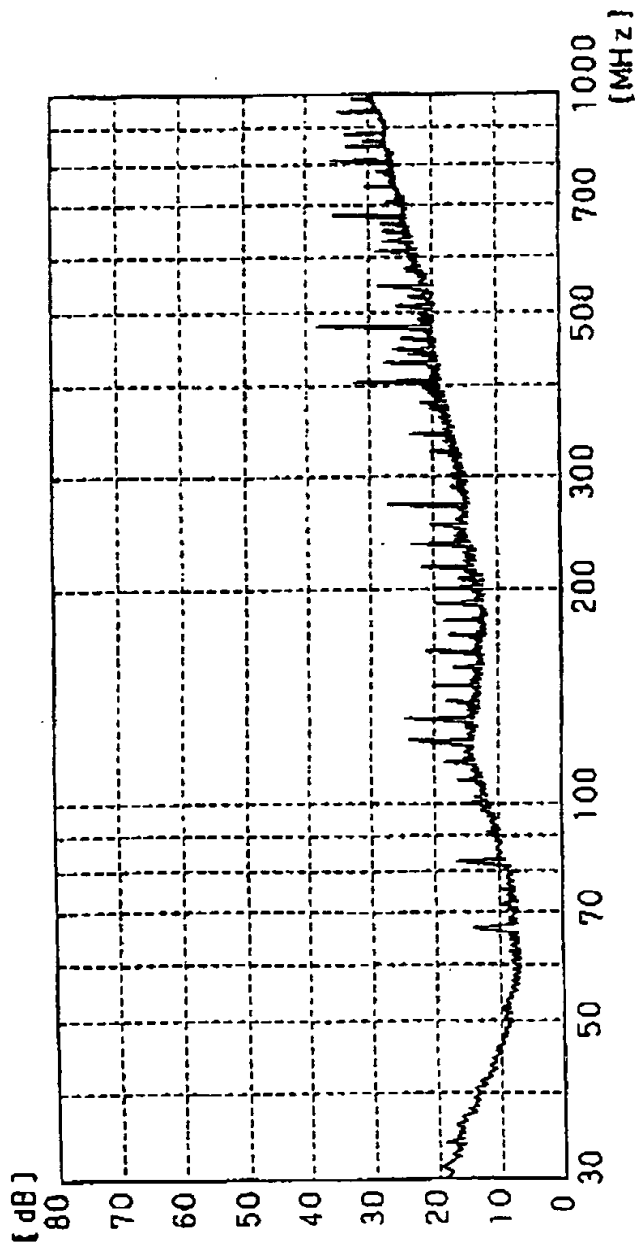
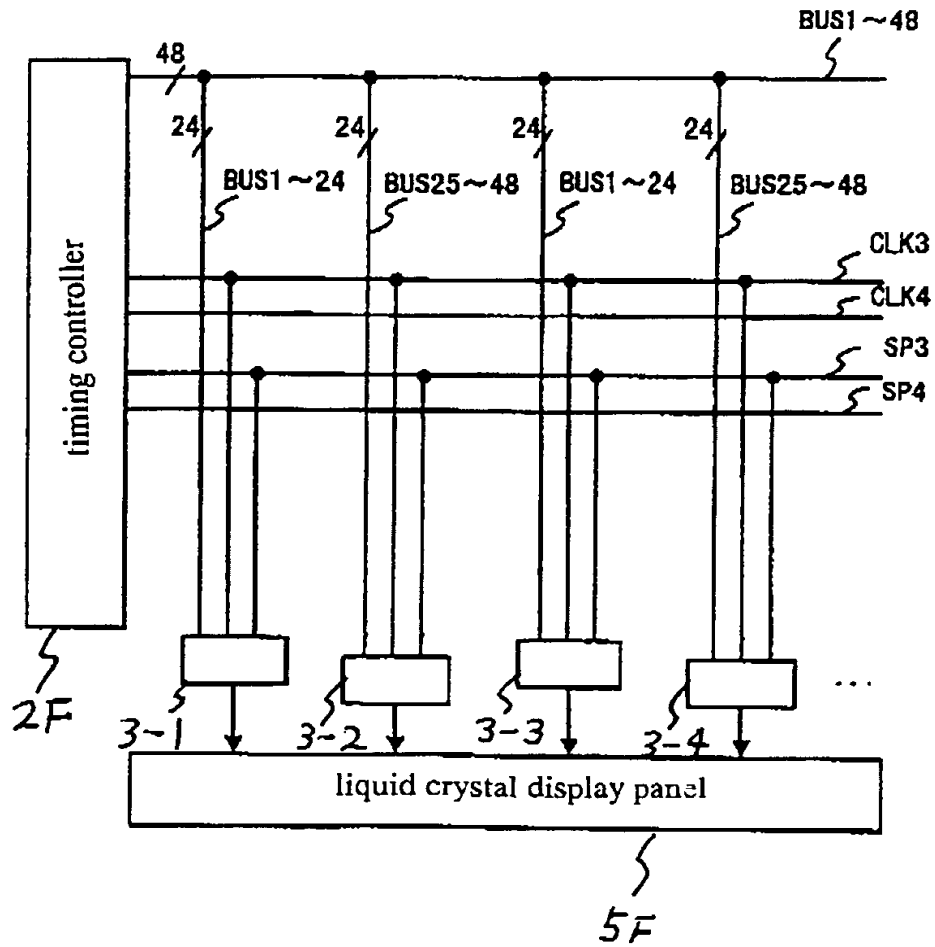


FIG. 40



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FIG. 41

